Mike Condel
Aaron Levinsky
ECE 429
Fall 04
Final Project

Tree Multiplication Implementation with Registers

Stage 1: 2 HALF ADDERS

Stage 2: 4 FULL ADDERS 1 HALF ADDER

Stage 3: 5 FULL ADDERS 2 HALF ADDERS
Stage 4: 7 FULL ADDERS
2 HALF ADDERS

Stage 5: 6 FULL ADDERS
2 HALF ADDERS

Stop here and implement registers
<table>
<thead>
<tr>
<th>Stage</th>
<th>Description</th>
<th>LSB Calculation</th>
<th>MSB Calculation</th>
</tr>
</thead>
<tbody>
<tr>
<td>6</td>
<td>6 FULL ADDERS</td>
<td>35 FULL ADDERS</td>
<td>13 FULL ADDERS</td>
</tr>
<tr>
<td></td>
<td>During Next Clock Cycle</td>
<td>(first clock cycle)</td>
<td>(second clock cycle)</td>
</tr>
</tbody>
</table>